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By: [Signature]

Date: Jan 11, 2002

2823
#21/Amend
2/8/02

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Thomas Huttner et al.
Applic. No. : 09/313,424
Filed : May 17, 1999
Title : SOI Semiconductor Configuration And Method Of
Fabricating The Same
Examiner : Brook Kebede
Group Art Unit : 2823

A M E N D M E N T

Hon. Commissioner of Patents and Trademarks,
Washington, D. C. 20231

S i r :

Responsive to the non-final Office action dated October
2001, kindly amend the above-identified application as
follows:

In the Specification:

Replace the paragraphs on page 12, lines 1-12, with --

Figs. 3a to 3f are diagrammatic vertical sectional views of a
sequence with a third design variant of the invention, in

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